

FEB 27 2006

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

IQ MODULATION SYSTEMS AND METHODS THAT USE SEPARATE  
PHASE AND AMPLITUDE SIGNAL PATHS AND PERFORM  
MODULATION WITHIN A PHASE LOCKED LOOP

Application Number: 09/746823

\*09/746823\*

Confirmation Number: 8910

First Named Applicant: Erik Bengtsson

Attorney Docket Number: 8194-453IP

Art Unit: 2631

Examiner: Khanh C. Tran

Search string: ( 6259747 or 6018275 or 20030143960 or 20020067773 ).pn.

Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:

That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
KCT	1	6259747	2001-07-10	Gustafsson et al.		375	298
KCT	2	6018275	2000-01-25	Perrett et al.		332	127

## US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
KCT	1	20030143960	2003-07-31	Yamawaki et al.		455	86

KCT	2	20020067773	2002-06-06	Jackson et al.	375	308
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Signature

Examiner Name	Date
Thanh Cong Tran	03/28/2006